

Fraunhofer Institute for Integrated Systems and Device Technology IISB

# SiC Materials

# Substrates & Epilayers

SiC wafers in epitaxy and characterization lab © Daniel Karmann / Fraunhofer IISB

# Customizing the Center Piece of all SiC Devices

Fraunhofer IISB has more than 15 years of experience in SiC epigrowth and defect inspection. Today, we are running a joint lab with Aixtron on SiC epigrowth, equipped with the latest generation of Aixtron's planetary reactors as well as inspection and metrology tools. The reactors and tools are automated on cassette-to-cassette (C2C) base and allow for high through-put and fast feedback loops. We are offering epilayers, measurements, calibration standards, and know-how.

# **Epitaxy reactors**

- AIXTRON G5 WW-C in 8x150 mm configuration
- AIXTRON G10 in 9x150 mm and 6x200 mm configurations
- Equipped with latest hardware innovations and process optimizations
- Nitrogen (N) and Aluminum (Al) doping in a wide range for n- and p-type epilayers, respectively
- Single epilayers as well as epilayer stacks from 0.5 μm to more than 50 μm thickness

**15**+ Years Experience in SiC Epigrowth

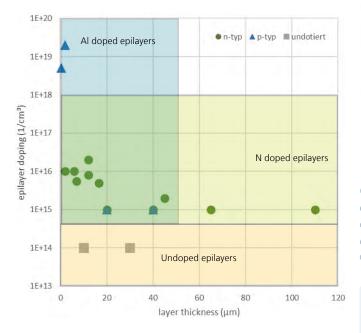
# Prototype foundry $\pi$ -Fab



Our stand-alone brand  $\pi$ -Fab - a prototyping service that includes a continuous SiC and silicon CMOS process line in an industry-compatible environment: www.iisb.fraunhofer.de/pi-fab.

#### **Epigrowth services**

- Small series (TRL 8) epigrowth processes, e.g., for 1.200 V devices with excellent w2w, r2r uniformities, attractive delivery times
- Product samples (TRL 6) for small series devices such as UV sensors
- Research samples (TRL 4) and demonstrators (TRL 2) for research projects
- Uniformities decreasing with TRL



Doping vs. thickness diagram with epitaxial process windows for n-type, p-type and nominally undoped epilayers. © Fraunhofer IISB

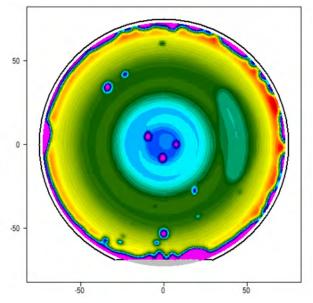
# Automated standard inspection & metrology tools

- All epiwafers pass through defect inspection and thickness / doping measurements
- Standard metrology tools are part of  $\ensuremath{\mathsf{w}}\pi\xspace$  environment
- Access to Lasertec SICA 88 for defect inspection in substrates and epilayers
- Semilab CnCV for doping concentrations and profiles

- Semilab EIR 2201 for epilayer thickness and uniformity
- Probers for doping concentrations and profiles (manual loading)

### Advanced inspection methods and tools

- X-ray topography (XRT) with Rigaku XRTmicron
- Carrier lifetime measurements by μ-PCD method
- Physical modelling of effective carrier lifetimes (TAUSim)
- Point defect characterization by Deep Level Transient Spectroscopy (DLTS)
- Semilab CnCV for defect inspection by QUAD mappings



Quality, Uniformity And Defect (QUAD) mapping of a standard epiwafer based on surface voltage differences. Concentric doping and thickness profiles can be seen as well as extended defects (spots) and the substrate's facet. © Fraunhofer IISB

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